

## AMENDMENTS TO THE CLAIMS

Please amend the claims as follows:

1. (Currently Amended) An apparatus comprising:  
a first audio input/output (I/O) connector provided for coupling to a first audio I/O device;  
~~at least one~~ a second audio input/output I/O connector provided for coupling to a second audio I/O device;  
the first and second connectors being coupled to an audio controller;  
~~a circuit coupling the first audio input/output connector to the audio controller;~~  
~~at least one circuit coupling at least one second audio input/output connector to the audio controller;~~  
a transistor coupled to the first and second connectors and to ground, the transistor connected to pull the first device coupled to the first I/O connector to a zero voltage level when the second device is coupled to the second I/O connector;  
~~an audio I/O device coupled to connect the first and second connectors to the audio controller, whereby the transistor pulls the device coupled to the first connector to a substantially zero voltage level when the device is coupled to the second connector; and~~  
a direct current blocking cap including a filter circuit coupled with an inverting amplifier, wherein the device is coupled ~~between the direct current blocking cap and a primary audio input/output coupling.~~  
a primary audio input disable signal coupling; and  
a disabling device and a circuit element, the circuit element coupling a filter circuit with an inverting amplifier providing a DC blocking cap,

whereby the primary audio input disable signal coupling, the disable device and the circuit element are coupled between the first and second I/O connectors.

2. (Currently Amended) The apparatus of Claim 1, ~~wherein the device electrically decoupling the first audio input/output connector from the circuit coupling the first audio input/output connector to the audio controller when an audio input/output device is coupled to at least one second input/output connector comprises a field effect transistor~~ further comprising an integrating amplifier.

3. (Currently Amended) The apparatus of Claim 2, ~~wherein the transistor comprises a drain, a source, and a gate, wherein the drain is coupled to the first audio input/output connector, the source is coupled to ground, and the gate is coupled to at least one second audio input/output connector such that current flows into the gate when an audio input/output device is coupled to a second audio input/output connector to which the gate is coupled~~ further comprising an audio I/O coupling.

4. (Cancelled).

5. (Cancelled).

6. (Original) The apparatus of Claim 1, wherein the first audio input/output connector comprises a jack.

7. (Original) The apparatus of Claim 1, wherein the second audio input/output connector comprises a jack.

8. (Currently Amended) A computer system, comprising:  
a processor;  
a memory coupled to the processor;  
an audio controller coupled to the processor;  
a first audio ~~input/output~~ I/O connector coupled to the audio controller and  
provided for coupling to a first audio I/O device;  
~~at least one~~ a second audio ~~input/output~~ I/O connector coupled to the audio  
controller and provided for coupling to a second audio I/O device;  
a transistor coupled to the first and second connectors and to ground, the  
transistor connected to pull the first device coupled to the first I/O  
connector to a zero voltage level when the second device is coupled to  
the second I/O connector;~~an audio I/O device coupled to connect the first~~  
~~and second connectors to the audio controller, whereby the transistor~~  
~~pulls the device coupled to the first connector to a substantially zero~~  
~~voltage level when the device is coupled to the second connector; and~~  
~~a direct current blocking cap including a filter circuit coupled with an inverting~~  
~~amplifier, wherein the device is coupled between the direct current~~  
~~blocking cap and a primary audio input/output coupling.~~  
a primary audio input disable signal coupling; and  
a disabling device and a circuit element, the circuit element coupling a filter  
circuit with an inverting amplifier providing a DC blocking cap,  
whereby the primary audio input disable signal coupling, the disable device and  
the circuit element are coupled between the first and second I/O  
connectors.

9. (Currently Amended) The computer system of Claim 8, ~~wherein the~~  
~~device electrically decoupling the first audio input/output connector from the circuit~~  
~~coupling the first audio input/output connector to the audio controller when an audio~~

~~input/output device is coupled to at least one second input/output connector comprises a field effect transistor further comprising an integrating amplifier.~~

10. (Currently Amended) The computer system of Claim 9, ~~wherein the transistor comprises a drain, a source, and a gate, wherein the drain is coupled to the first audio input/output connector, the source is coupled to ground, and the gate is coupled to at least one second audio input/output connector such that current flows into the gate when an audio input/output device is coupled to a second audio input/output connector to which the gate is coupled further comprising an audio I/O coupling.~~

11. (Cancelled).

12. (Cancelled).

13. (Original) The computer system of Claim 8, wherein the first audio input/output connector is a jack.

14. (Original) The computer system of Claim 13, wherein the second audio input/output connector comprises a jack.

15. (Original) The computer system of Claim 8, wherein the second audio input/output connector comprises a jack.

16. (Cancelled).